



NEW HORIZON COLLEGE OF ENGINEERING QUALITY ASSURANCE AND SKILL DEVELOPMENT CENTER

International Webinar

Topic : Data Science Applications using ML and DL Techniques
Resource persons :
Dr. Asadi Srinivasulu
Professor of IT & Head research
Data Science Research lab
BlueCrest University, Monrovia, Liberia.

Event Poster:



QUALITY ASSURANCE AND SKILL DEVELOPMENT CENTER
Presents
INTERNATIONAL WEBINAR
ON
**DATA SCIENCE APPLICATIONS
USING ML AND DL TECHNIQUES**

SPEAKER



DR. ASADI SRINIVASULU
Professor of IT & Head Research, Data Science Research lab,
BlueCrest University, Monrovia, Liberia.

ONLINE MODE
SCAN HERE TO JOIN



Dr. Rajesh G
Co-ordinator, ASP/ECE

Dr. Sanjeev Sharma
Dean- QASDC

Dr. Manjunatha
Principal

 10:00AM - 12:00PM

 2ND SEPTEMBER 2022



NEW HORIZON COLLEGE OF ENGINEERING QUALITY ASSURANCE AND SKILL DEVELOPMENT CENTER

Report on “Faculty Interaction with Industry”

Topic	: Development of FPGA based AI Product Design Techniques
Resource person	: Mr. Ashish K, Intel India Pvt. Ltd
Venue	: A308, MBA Dept
Date & Time	: 23rd July 2022 & 9.30AM
No of Participants	: 30 from ECE & EEE

The Quality Assurance and Skill Development Center at New Horizon College of Engineering organised a Faculty Interaction with Industry to improve NHCE-faculty technical skills and make them aware of the latest technologies and industry's support for academia. As part of this programme, an industry interaction on "Development of FPGA-based AI Product Design Techniques" will be held in A308, MBA Lecture Hall. The expert speaker was Mr. Ashish K, Interface IP Engineer Intel India Ltd.

The welcome address was given by Dr. Sanjeev Sharma, Dean QASDC, who explained the significance of the programme. Mr. Ashish K spoke about the role of industrial products in delivering technology that has an impact on our world. Intel's solutions for enabling innovation from the edge to the cloud in industrial automation are discussed. With Intel® FPGAs, industries can create designs that are safe, reliable, and optimised. Intel offers a diverse range of FPGA and ARM® core integrated SoC devices, tools, IPs, reference solutions, and so on to support a variety of application use cases such as artificial intelligence, edge computing, and robotics.

The faculty interacted with the expert Speaker for clarifications to proceed with research work and requested to conduct a hands-on session.



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Figure 1: Glimpses of the Event

Finally, Dr. Rajesh G, (Associate Professor ECE and Coordinator of the event) delivered vote-of thanks and winded up the session by 11:15AM.

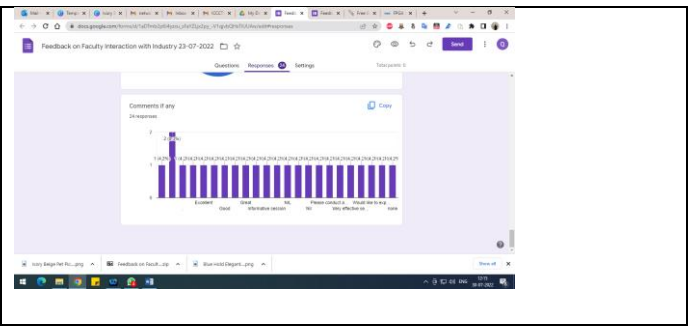
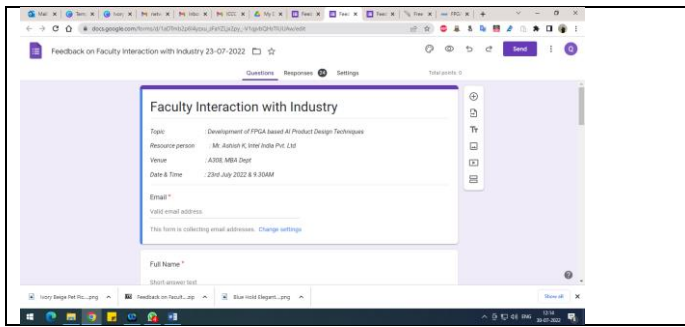


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Attendance and Feedback Analysis:

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Impact:

- The participants interacted with the resource person for further developments/revisions in curriculum which can be made for the benefit of students.