NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Academic Year 2019-20
Total Hours Engaged: 50 Hours

Visiting Faculty Session Details for the Academic Year 2019-2020

Sl. No	Name of the Industrial Expert	Company/ Organization	Designation	Topic /Subject	Contribution to Curriculum	Semester	Hours engaged	Date
1	Ms. Tabassun V Mulla	Invecas Technologies	Hardware Design Engineer	Timing Analysis in MOS circuits	Delivered Theoretical session for the subject CMOS VLSI Design	V	3	23-08-2019
2	Mr. Prafulla ShyamKant Galphade	Cadence design systems	Senior Member of Technical staff	Sequential Circuits	Delivered Theoretical session for the subject Digital Electronic Circuits	III	3	23-08-2019
3	Mr.Vaishak Sundaresh	Freelance Corporate trainer	Senior Principal Program Manager	Programming with data structures using C	Delivered Theoretical session for the subject data structures using C	V	3	23-10-2019
4	Mr. Gobala kichenan Ganeshan	IBM india Pvt. Ltd	NA	Microcontroller and its significance in real world	Delivered Theoretical session for the subject Microcontrollers	V	3	11-11-2019
5	Mr. Prafulla Shyam Kant Galphade	Cadence design systems	Program Manager	FPGA	Delivered Theoretical session for the subject System design using HDL	IV	3	25-01-2020

6	Mr. Praveen Kumar	Ekagga technology and services pvt Ltd	Senior Principal Program Manager	ARM programming (assembly c programming on android devices)	*Delivered* Theoretical session for the subject Embedded System	VI	3	10-02-2020
7	Mr. Galphade Prafulla	Cadence Design Systems, Bangalore	Founder and CEO	FPGA	Delivered Theoretical session for the subject System design using HDL	IV	3	03-02-2020
8	Mr. Praveen Kumar	Ekagga Technology & Services Pvt Ltd.	Senior Principal Program Manager	Arm Programming on Android Devices	Delivered Theoretical session for the subject Embedded System	VI	3	15-02-2020
9	Mr. Jyotirmoy Koner	Bharat Electronics Ltd, Bangalore	Founder & CEO	Microwave Active & Passive Devices	Delivered Theoretical session for the subject Microwave and Radar	VI	3	20-02-2020
10	Mr. Harish Kumar Villuri	Intel Technologies	Component Design Engineer	CMOS VLSI Design (Timing Analysis)	Delivered Theoretical session for the subject CMOS VLSI Design	V	3	20-09-2019
11	Mr. A Athif Shan	ABE Semiconductor Designs	Chairman/ Managing Director	Multi core architecture and communication	Delivered Theoretical session for the subject Wireless Mobile Communic ation	V	3	13-10-2019
12	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Network Analysis (Module 5: Waveform Synthesis)	Delivered Theoretical session for the subject Network Analysis	III	3	25-10-2019

13	Mr. Prafulla ShyamKant Galphade	Invecas Technologies	Principal Member technical staff	Digital Electronic Circuits (Module 5)	Delivered Theoretical session for the subject Digital Electronic Circuits	III	3	17-10-2019
14	Mr. Sunder Murthy	LS Control Systems	Proprietor	Microcontroller (Module 5, Interfacing)	Delivered Theoretical session for the subject Microcontrollers	V	3	13-10-2019
15	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Signals and Systems (Module 5)	Delivered Theoretical session for the subject Signals & Systems	III	3	14-11-2019
16	Ms Jayachandran Aradhya	Silicon Microsystem, Bangalore	Chief Executive	Antenna Design	Delivered Theoretical session for the subject Antenna & Wave Propagation	VII	3	14-11-2019
17	Ms. Poornima Mohanachandran	Ekalakshya	CEO	Analog Electronics Product Development	Delivered Theoretical session for the subject Electronic Devices and Circuits	III	3	07-11-2019

Coordinator   **HOD**