

NEW HORIZON COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Academic Year 2018-19
Total Hours Engaged: 51 Hours

Visiting Faculty Session Details for the Academic Year 2018-2019

Sl. No	Name of the Industrial Expert	Company/ Organization	Designation	Topic /Subject	Contribution to Curriculum	Sem ester	Hours engaged	Date
1	Mr. Harish Kumar Villuri	Intel Technologies	Component Design Engineer	CMOS VLSI Design (Timing Analysis)	Delivered Theoretical session for the subject CMOS VLSI Design	V	3	20-09-2018
2	Mr. A Athif Shan	ABE Semiconductor Designs	Chairman/ Managing Director	Multi core architecture and communication	Delivered Theoretical session for the subject Microprocessor & Microcontrollers	V	3	13-10-2018
3	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Network Analysis (Module 5: Waveform Synthesis)	Delivered Theoretical session for the subject Network Analysis	III	3	25-10-2018
4	Mr. Prafulla ShyamKant Galphade	Invecas Technologies	Principal Member technical staff	Digital Electronic Circuits (Module 5)	Delivered Theoretical session for the subject Digital Electronic Circuits	III	3	17-10-2018
5	Mr. Sunder Murthy	LS Control Systems	Proprietor	Microcontroller (Module 5, Interfacing)	Delivered Theoretical session for the subject Microcontrollers	V	3	13-10-2018
6	Mr. Arun Krishnan	Harman India	Senior Product Development Engineer	Signals and Systems (Module 5)	Delivered Theoretical session for the subject Signals & Systems	III	3	14-11-2018
7	Ms Jayachandran Aradhya	Silicon Microsystem,	Chief Executive	Antenna Design	Delivered Theoretical session for the subject	VII	3	14-11-2018

		Bangalore			Antenna & Wave Propagation			
8	Ms. Poornima Mohanachandran	Ekalakshya	CEO	Analog Electronics Product Development	Delivered Theoretical session for the subject Electronic Devices and Circuits	III	3	07-11-2018
9	D Suresh	Renesas Electronics	System Analyst	Wireless and mobile communication	Delivered Theoretical session for the subject Wireless Mobile Communication	VII	3	03-11-2018
10	Mr. Sarang Suresh Akotkar, Mr Kumaran Sethu Raman	Intel Technology	R & D Engineer	DSP Processor & Digital Filters	Delivered Theoretical session for the subject Digital Signal Processing	IV	3	16-04-2019
11	Mr. Shashikanth Patil	Wipro Technology	Senior Project Engineer	Synthesis on FPGA	Delivered Theoretical session for the subject System design using HDL	IV	3	13-04-2019
12	Mr.Muralitharan	Emids technologies	IOS Developer	Exception Handling	Delivered Theoretical session for the subject Object Oriented Programming	VI	3	04-04-2019
13	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	VLSI-transistor to transformation	General talk in recent advancements in VLSI	IV	3	09-02-2019
14	Mr. Bipin Malhan	Invecas Technologies	Managing Director	Career growth in electronics Industry	General talk about recent innovations and developments in electronics Industry	VII	3	24-10-2018
15	Mr. Akshatha Pai	Sirena Technologies	Project Manager	Robotics; History, Current trends, future	General talk on current trends in robotics	V	3	22-09-2018
16	Ms. Neha Bharti	Altran	ASIC Design	Pathway to semiconductor	General talk on career	VII	2	18-08-

		Technologies	Engineer	Industry	opportunities in semiconductor industry			2018
17	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	Hardware Design flow for Chip Design	Delivered Theoretical session for the subject CMOS VLSI Design	V	2	11-08-2018
18	Mr.Shriram S	Intel Technologies	Hardware Design Engineer	Art of Electronics	General talk about product development in electronics- steps and process flow	III	2	11-08-2018


Coordinator


HOD