

Academic Year 2020-21
Total Hours Engaged: 45 Hours

Sl. No	Name of the Industrial Expert	Company/Organization	Designation	Topic/Subject	Contribution to Curriculum	Semester	Hours engaged	Date
1	Mr. Tejas Dunakhe	Hytech Automation	GM,Sales and Marketing	Artificial Intelligence in Robotics	Delivered Theoretical session for the subject Artificial Intelligence	VII	3	14-12-2020
2	Mr.Prafulla Galphade	Cadence design system	Senior principal program manager	Finite State Machine	Delivered Theoretical session for the subject Digital Electronics Circuits	III	3	21-12-2020
3	Mr. Kundana Lal	Vitti Research Foundation	President	Rise of Artificial intelligence and its impact on social and economic fabric	Delivered Theoretical session for the subject Artificial Intelligence and cognitive computing	V	4	22-12-20
4	Mr. Manas Guptha	wealth Technology, Australia New Zealand Bank	Head	Innovations in AI and ML	Delivered Theoretical session for the subject Machine Learning	VII	3	24-12-20
5	Ms. Soumya Agarwal	Cisco	Software engineer	Chaotic Images	Delivered Theoretical session for the subject Network security	VII	6	29-12-20
6.	Mr. Ram Kumar	Wells Fargo	Application System Engineer	VLSI Design	Delivered Theoretical session for the subject CMOS VLSI	V	4	6-10-2020

7	Mr. Praveen Kumar	Ekkaga Technology and services	Founder and CEO	8051 programming	Delivered Theoretical session for the subject Microprocessors	V	4	10-11-2020
8	Prof H S Bhatia	IMAPS, India	chairman	Introduction to RADAR Systems	Delivered Theoretical session for the subject Microwave and RADAR systems	IV and VI	3	19-5-21
9	Mr. Ganesh Attarde	GB Electronics	Founder	Singly Linked List Data Structure	Delivered Theoretical session for the subject OOPS	VI	3	21-5-21
10	Dr. P.Subbanna Bhat	Anvation labs ,Bangalore	chief mentor	OP-AMP APPLICATION	Delivered Theoretical session for the subject Linear Integrated Circuits	IV	3	01-6-21
11	Ms. Priyanka .B. Gaikwad	Welldoc, India	software developer	Introduction to Android App Development	Delivered Theoretical session for the subject Wireless and mobile communication	VII	3	12-6-21
12	Mr. Ashish Khachane	Xilinx Productline Excelpoint Systems India Pte Ltd	Assistant Manager	FPGA ARCHITECTURES	Delivered Theoretical session for the subject HDL	IV	3	10/7/21
13	Mr.Prafulla Galphade	cadence Design systems(india)pvt Ltd	senior principal program manager	FPGA ARCHITECTURES	Delivered Theoretical session for the subject HDL	IV	3	17/7/21