

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

30/01/2020

GUEST LECTURE

This is to inform all IV semester students of ECE that a guest lecture is arranged on <u>03/02/2020</u> at <u>Falconry Seminar Hall</u> between <u>10:00am to 12:00 pm</u>.

Topic to be delivered : FPGA

Name of the Speaker : Mr. GalphadePrafulla

Designation : Senior Principal Program Manager

Company : Cadence Design Systems

Respective course teachers are hereby informed to be present at Falconry Seminar Hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

HOD-ECE

Biodata

PrafullaGalphade

Mr. PrafullaGalphade is working as 'Senior Principal Program Manager' at Cadence design systems (India) Pvt. limited. At his previous company, Invecas, he led Memory Design Team and managed Memory Program at Bangalore Design center. He has 17+ years of experience in FPGA, Analog & Memory Design. His area of interest is CAD, Scripting and Design Automation.

Prafulla has done B.E. in Electronics Engineering, MBA, both from Nagpur University and M. Tech from SardarVallabhbhai National Institute of Technology, Surat. He started his career as coordinator at C-DAC and later joined the Industry. He has worked for Infineon Technologies, Emerging Memory Technologies and Intel. He is still in love with teaching, and has delivered lectures in many training programs and workshops.

NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem Attendance , Guest Lecture on 3 Feb 2020 , Falconry Hall

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION

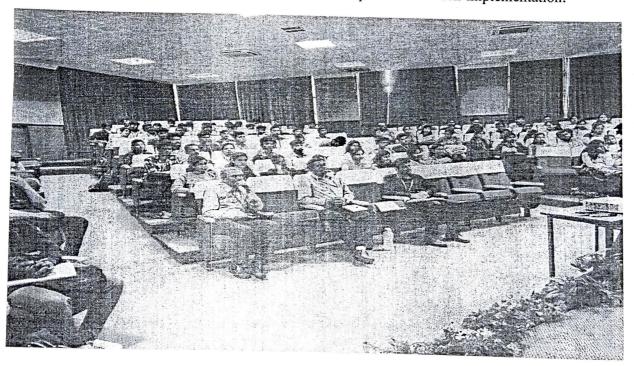
REPORT ON GUEST LECTURE

TOPIC: FPGA TECHNOLOGY

The department of Electronics and Communication of New Horizon College of Engineering had organized a guest lecture on 3 February 2020 for the fourth semester students.

The lecture on the topic "FPGA TECHNOLOGY" was provided by Mr.Prafulla Galphade, senior principal program manager, Cadence Design Systems (private) Ltd. The resource person gave a two hour presentation on various FPGA technologies, their significance, working and industrial applications.

The session started with him giving an introduction on Programmable Logic Devices, their types and a briefing about the need for them; and also about look up tables and their implementation.



This was followed by an introduction to what an FPGA is, the FPGA design flow (basic architecture), its evolution and the testing process. Mr. Galphade further went on to elaborate on the topic by discussing the

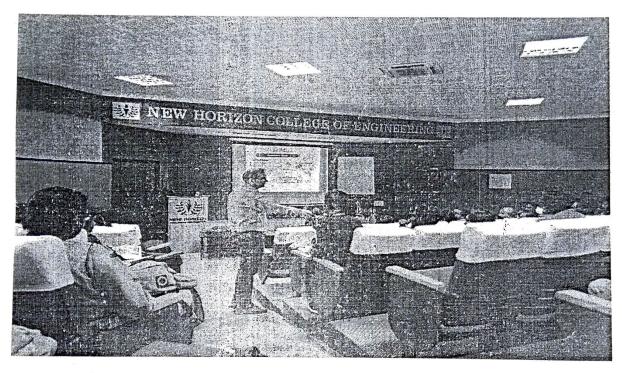
process of FPGA synthesis considering the basic examples like logic synthesis, behavioral synthesis, etc and the synthesis tools.

The next subtopic covered by the speaker was the 'Circuit Compilation Of FPGA' which included explanations on:

- 1) Circuit Netlist and Mapping
- 2) The Placement Metrics
- 3) The Routing Process

This was followed by a discussion on the application of the available FPGA technologies in various industrial aspects which mainly covered points on the appropriate technologies adapted by the industry along with a briefing about the advantages and disadvantages of each of them.

All of these above mentioned topics where clearly explained to the students with the help of presentations. The session was an interactive one wherein the students were asked questions by Mr.Galphade relevant to various topics that he covered. He also spoke about the industry expectations of upcoming engineers.



After his presentation concluded, the students were given the chance to voice out their questions on the topics that was covered and also regarding the upcoming developments and requirements in modern technology.

The session was indeed a beneficial one which helped the students understand the perspectives of the subject.



The necession coincluded with a constitution of amountation of a momento to Mr Galphade for taking his time out to enlighten the students.