

Department of Electronics and Communication Engineering

20.04.2018

# **EXPERT LECTURE**

The ECE students are hereby informed that **Expert Lecture** is scheduled on

"555 timer and its Applications, ALD1502 and ICL8038"
On 21.04.2018.

By

Ms Dhanaselvi D

Project Engineer, ADA, Bangalore

Timing schedule is as follows

1	2:30-4:30	4 <sup>th</sup> (A, B&C)
S. No	Timings	Semester

Respective subject teachers are hereby informed to be present in the Seminar Hall during the schedule.

Note: All Faculty members are invited to attend the same (without affecting regular classes)

Venue: Chanakya Seminar hall

14-

Expert Lecture Faculty Incharge

HoD - ECE

## PERSONAL DATA

Name

: Dhanaselvi D.

Designation

: Project Engineer

Organisation

: ADA

Email

: dhanselvi1979@gmail.com

Mobile No

: 9535664773



### Education:

- Completed M.E in Embedded System Design from Anna University, Coimbatore in 2011.
- Completed B.E in Electronics & Communication from Anna University, Vellore in 2001.

## **Professional Details**

- > 14 years of experience in Avionics, Automotive and Medical Equipment software / Design, Development and verification.
- > A proactive leader with abilities in training and motivating cross cultural teams.
- Experienced in ISO-9001-2000 Co-ordination and conduct audit on the industrial training.
- Experienced in corporate training conducted for small scale industries and outside academic Institution.
- Experience in DO-178B Guidelines and DOD-2167a Software Testing Life Cycle of Iterative V-Model for avionics S/W of level A, B & C.
- Experience in H/W S/W Integration Testing and S/W S/W Integration Testing, Unit level testing, MBSE using LDRA & Ada Multi and IBM Rational Rhapsody, Cameo System Model & No Magic.
- Experience in DO-254 Standard and prepared Guideline for Development Life Cycle of FPGA based design and formally released to DRDO.
- ➤ Worked on configuration management IBM Rational clear case and GIT
- Experience in problem reporting and analysis with tool exposure on Mentor Graphics Questa Prime, Xilinx.
- Experience in Weapon Management System (SIB & PIB), Ghatak, UPIC, ASA-ARU (LRU s) and truck EDC-7 and EDC-17 of Korean ECU s.
- Exposure to FPGA Virtex-5 and Artex-7.



Sachin V <sachuv91@gmail.com>

## Regarding expert lecture

7 messages

Sachin V <sachuv91@gmail.com> To: dhanaselvi@jetmail.ada.gov.in

Tue, Apr 3, 2018 at 1:49 PM

Hi.

As  $^{\text{discussed}}$ , our HOD has agreed to keep the expert lecture on 21st of this month at 2:30 to 4:30. We require your CV and photo for further processing.

Regards, Sachin

Dhanaselvi D <dhanaselvi@jetmail.ada.gov.in>
To: Sachin V <sachuv91@gmail.com>

Tue, Apr 10, 2018 at 4:43 PM

Dear Sachin,

I have attached my profile.

Regards,

D.Selvi

From: Sachin V [mailto:sachuv91@gmail.com]

Sent: Tuesday, April 03, 2018 1:50 PM To: dhanaselvi@jetmail.ada.gov.in Subject: Regarding expert lecture

[Quoted text hidden]

### 2 attachments



**IMG\_0010.jpg** 28K

Selvi.pdf 82K

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### NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "A" Sec

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# NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING IV Sem "B" Sec

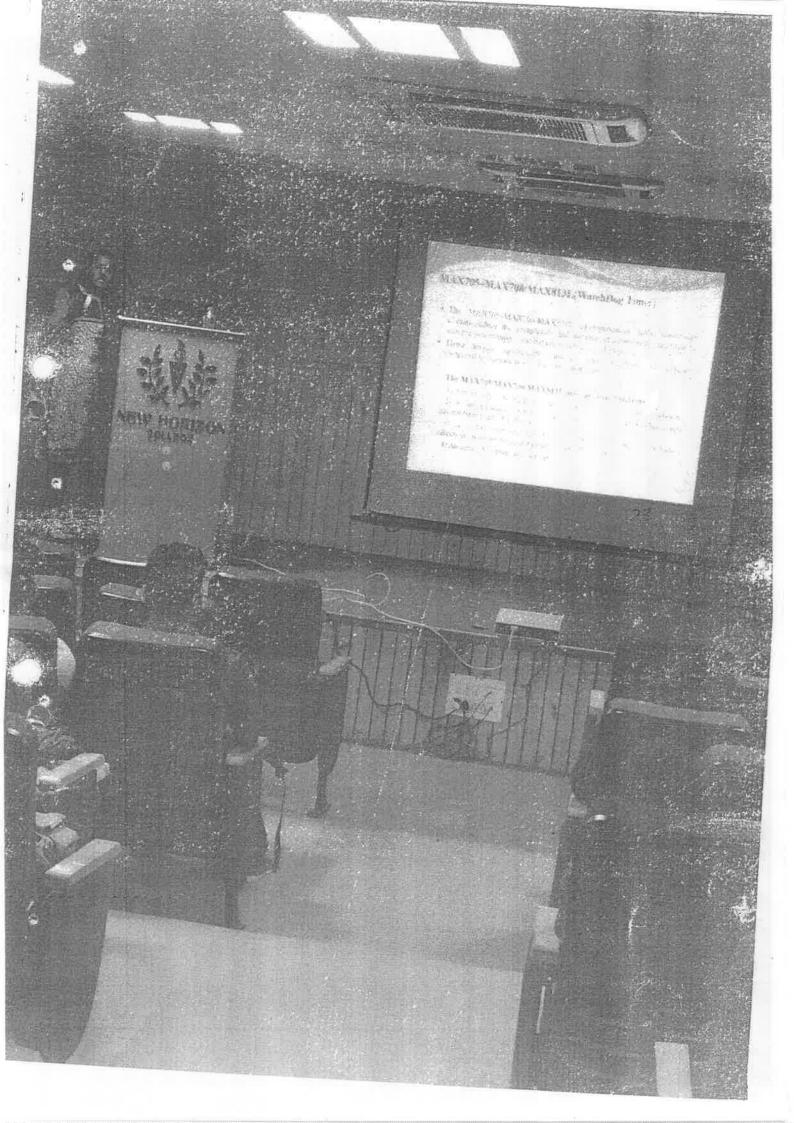
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### REPORT ON GUEST LECTURE BASED ON 555 TIMER IC

The 555 timer IC is an integrated circuit (chip) used in a variety of timer, pulse generation, and oscillator applications. The 555 can be used to provide time delays, as an oscillator, and as a flip-flop element. Derivatives provide two (556) or four (558) timing circuits in one package.

Introduced in 1972 by Signetics, the 555 is still in widespread use due to its low price, ease of use, and stability. It is now made by many companies in the original bipolar and in low-power CMOS technologies. As of 2003, it was estimated that 1 billion units were manufactured every year. The 555 is the most popular integrated circuit ever manufactured.

## Design

Depending on the manufacturer, the standard 555 package includes 25 transistors, 2 diodes and 15 resistors on a silicon chip installed in an 8-pin dual in-line package (DIP-8). Variants available include the 556 (a DIP-14 combining two complete 555s on one chip), and 558 / 559 (both a DIP-16 combining four reduced-functionality timers on one chip).

The **NE555** parts were commercial temperature range, 0 °C to +70 °C, and the **SE555** part number designated the military temperature range, -55 °C to +125 °C. These were available in both high-reliability metal can (T package) and inexpensive epoxy plastic (V package) packages. Thus the full part numbers were NE555V, NE555T, SE555V, and SE555T.

Low-power CMOS versions of the 555 are also available, such as the Intersil ICM7555 and Texas Instruments LMC555, TLC555, TLC551. CMOS timers use significantly less power than bipolar timers, also CMOS timers cause less supply noise than bipolar version when the output switches states. The ICM7555 datasheet claims that it usually doesn't require a "control" capacitor and in many cases does not require a decoupling capacitor across the power supply pins. For good design practices, a decoupling capacitor should be included, however, because noise produced by the timer or variation in power supply voltage might interfere with other parts of a circuit or influence its threshold voltages.

## Modes

The IC 555 has three operating modes:

1. **Astable** (free-running) mode – the 555 can operate as an electronic oscillator. Uses include LED and lamp flashers, pulse generation, logic clocks, tone generation, security alarms, pulse position modulation and so on. The 555 can be used as a simple ADC, converting an analog value to a pulse length (e.g., selecting a thermistor as timing resistor allows the use of the 555 in a temperature sensor and the period of the output pulse is determined by the temperature). The use of a

- microprocessor-based circuit can then convert the pulse period to temperature, linearize it and even provide calibration means.
- 2. **Monostable** mode in this mode, the 555 functions as a "one-shot" pulse generator. Applications include timers, missing pulse detection, bounce-free switches, touch switches, frequency divider, capacitance measurement, pulse-width modulation (PWM) and so on.
- 3. **Bistable** (schmitt trigger) mode the 555 can operate as a flip-flop, if the DIS pin is not connected and no capacitor is used. Uses include bounce-free latched switches.

## Simple 555 Timer Circuits and Applications

There are many applications of 555 timers. Here as an example we will discuss 555 Timers used in Lamp Dimmer, Wiper Speed control, Timer Switch, Variable duty cycle fixed frequency 555 oscillator etc. You can open any of these circuits and edit it to you want.

#### .NE555 Astable

NE555 is configured in a stable (bistable) mode, due to the pin 3 of the IC is a coupled MOSFET or (if you want, it can also be a power transistor that matches the pins of the MOSFET), you can connect a bigger load such as DC motors or 12VDC bulbs to adjust the light intensity or speed of rotation by potentiometer.

### Lamp Dimmer using NE555

This project is about simple lamp dimmer project using NE555 timer IC. PWM method is used for controlling the brightness of the lamp. This method is very power efficient and low cost compared to linear power control circuits. In PWM method the load is driven using a high frequency square wave and the duty cycle of this square wave is varied for controlling the power delivered to the load. The efficiency of this circuit was found to be 95.5% when tested in the lab. The same circuit can be also used to control the speed of DC motors.

### Wiper Speed control using NE555

This project is about a simple automobile wiper speed control. The speed of the automobile wiper can be adjusted using a potentiometer using this circuit. The circuit operates from 12V DC and can be fitted to any automobile operating on 12V electrical system. With slight modification, the same circuit can be made to operate on 24V systems also.

Panh Die