Department of Electronics and Communication Engineering

20.02.2018

GUEST LECTURE

The ECE students are hereby informed that a guest lecture is scheduled on

"Trends in Communication System Design" on 22.02.2018.

By

Mr. Raveendranath K.R

Lekha Wireless Technologies, Bangalore.

Timing schedule is as follows

S. No	Timings	Semester	
1	09.30 AM - 11.00 AM	8	

Respective subject teachers are hereby informed to be present in MBA Seminar Hall during the schedule.

Note: Interested Faculty members are invited to attend the same (without affecting regular classes)

Venue: MBA Seminar Hall

Guest Lecture Faculty in Charge

HOD ECE

Franch Li



Summary

Director - RF & FPGA Systems

20 years of Technical management and hands-on experience in architecting and building Hardware and FPGA systems for communication and Embedded products.

Core competence in Hardware System R&D, Product design and management.

Core Strengths and key responsibility areas

- Led several product development cycles from evolving specifications to deployment and manufacturing
- Good grasp of overall hardware system design FPGA development, RF Signal chain designs and integration and Digital board designs
- Led design efforts for several RTL IP cores, interfaces, IO controllers.
- Good understanding and hands-on experience in board design and bring up of RF Transceivers and signal chains involving PA's, LNA's, Switches, Duplexers etc. Experience in characterization of RF signal chains and analysis.
- Expertise in HW-SW co-design, design partitioning, Resource estimation and choice of devices, hardware prototyping and System bring-up and characterization
- Strong system level expertise including mechanical and thermal design, design optimizations, design for manufacture, test and compliances
- Project Management, Customer interface, support and maintenance. Have handled several product design
 cycles for both Corporate and defense customers.

Technical Summary

- Led design teams in building several wireless communication IP's in LTE, DVB and WiMAX
- Led target design and bring-up efforts and integration of IP cores and RF transceivers
- Built systems around variety of architectures right from MPSoC/ Zynq, Ultrascale Kintex series to low power devices in 7 series, Virtex and Kintex, Spartan 3a dsp from Xilinx, Cyclone family from Altera, MachXO2280 and various CPLD and PAL devices
- Strong expertise in hardware and FPGA design flows
- Led several hardware design projects for mobile computing, wireline telephony and VOIP products

Key Project Contributions

At Lekha Wireless

Primary responsibility has been building a high competence development team for delivery of FPGA and communication hardware for the Lekha product line and customer projects.

- Currently leading a team and responsible for platform delivery for a large LTE Macro-cell productization program. Responsibility includes programmable logic, infrastructure for the signal chain on the platform, RF front end design, and hardware system integration including mechanical design and thermal
- Led a development team for design and development of RTL IP Core for a DVB receiver on a Kintex FPGA
- Have led a RTL development team for LTE UE physical layer components (PUSCH, PUCCH, PRACH, Receive Synchronization, RFE and FEC blocks: Turbo and Viterbi) and FPGA (Xilinx Zynq) integration of L1-L2 interface
- Architecture and development of FPGA based DSSS data link
- Development of RF transceiver sub-system for a Lekha product (ACT) for broad band data link applications
- FPGA customization and feature additions on MachXO2280 device on the WiMAX CPE platform.
- Development of a Radio front end board for 32dBm output power for TDD and FDD modes of operation for
- Board design involving Baseband FPGA and AD9361 for a communication equipment
- Development of Radio card based on AD9352 for Lekha WiMAX CPE solution
- Leading FPGA efforts for maintenance and feature enhancements for a customer in WiMAX technology
- Leading development effort for FPGA and Hardware design for a Kintex 160 FPGA based wided band radio
- Rapid prototyping of base band building blocks of a next generation MIMO system for a R&D customer
- Feature development and hardware integration for a test equipment for a customer in GPS technology
- Project management and customer interactions

Previous Experience [Select Case-studies only]

- Hardware and CPLD design for SATHI a battle field navigation and communication device
- DSP line modem for telemetry and tele-control applications developed for Fuji Electric, Japan
- Hardware and FPGA design for secure fax relay for an Indian customer based on TI DSP 5510 and Spartan 6 IP phone project based on DSP LSI403LP for Verisilicon
- Reference design for a proprietary silicon (SoC) for DRDO with various peripherals implemented in Cyclone
- Design of various mobile computing product variants for different applications. Part of the hardware design team for the Simputer – first Indian touch based handheld design in 2001. Various customer specific project accounts

Employment Summary

010 till date	Lekha Wireless Solutions
000 - 2010	Director heading the EPGA and BELL
1998 – 2000	Manager – Hardware Design at Engage 5 5
	Manager – Hardware Design at Encore Software Ltd., Bangalore Member – R&D
	TVS Electronics, Chennai

Education

- BE in Instrumentation & Electronics from Bangalore University \boldsymbol{X} Rank
- MS in VLSI CAD from Manipal University First class with Distinction

Membership

Member of Institution of Electronics and Telecommunication Engineers

Publications

- IEEE Milcom 2016, "Agile Cognizant Transceiver link for Mission Critical Applications", Amarnadha Reddy Mahasamudram, Ramu Srinivasaiah, Raveendranath KR, Sreenath Ramanath Lekha Wireless Solutions,
- Number of application notes and internal technical notes on variety of topics

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VIII Sem "C" Sec

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VIII Sem "B" Sec

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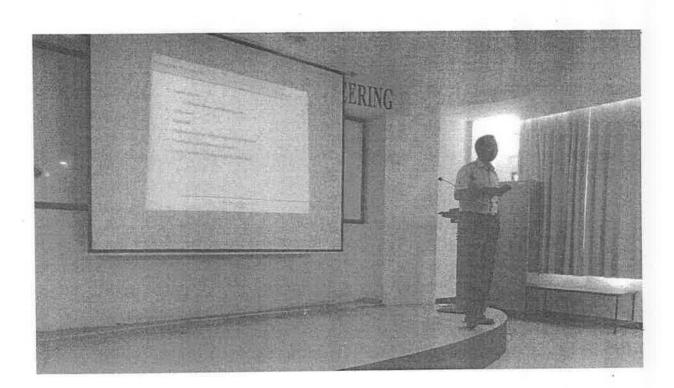
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Department of Electronics and Communication Engineering

REPORT ON GUEST LECTURE

The Department of ECE conducted a Guest Lecture on "Recent Trends in Communication System Design" on 22/02/2018 by Mr. Raveendranath K R, Director, RF and FPGA systems, Lekha Wirelesss Technologies, Bangalore for the students of 8th semester. The speaker concentrated on the fundamentals of the communication field. He mentioned that, technology can change from time to time, but the fundamentals remain unchanged. The interaction was very lively and the students were able to grasp the topic as he discussed various real life examples. The session also gave an insight into the noise margin, sensitivity and other parameters of a communication system. The speaker also gave an introduction to different domains in communication and various aspects to be taken care of while designing a real time communication system.

Photograph



GUEST LECTURE COORDINATOR

Arashell HOD

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