Kulbhushan Bhaji Patariya

Email: Kulbhushan@jetmail.ada.gov.in Phone: (M) + 91 8861637637

Professional Experience

- Working in Aeronautical Development Agency (ABA), Bangalore as Scientist since August 2013

Skill set

Software Languages

: C, Assembly, ADA 83/95, VHDL, Verilog

Protocol Used

: MIL-1553B, RS-422,

Standards/Guidelines

: DO-178B, DO-254, DOD-2167A, MISRA C, VHDL & Ada Coding

- Tools

: LDRA, Mentor Graphics Questa Prime, MagicDraw, Xilinx,

Cameo System Modeler, Ada Multi 2000

- Configuration Tools

: IBM Rational Clear case, GIT

Academic Qualification

- M.Tech in Power Electronics and Power System from IIT Bombay.
- B. Tech. In Computer Science and Engineering. From Madan Mohan Malaviya Engineering College

Professional Details

- 8 years of experience in Software verification of Avionics Line Replacement unit
- Experienced in DO-178B and DOD-2167A Standards in Software Testing Life Cycle of Iterative V-Model for avionics S/W of level A, B & C.
- Experienced in H/W S/W Integration Testing and Unit level testing.
- Experienced in DO-254 and prepared Guidelines for Development Life Cycle of FPGA based design and formally released to DRDO.
- Contributed towards the development of "DRDO standard for Software Development Guideline". This guideline is developed by DRDO for software development to be followed throughout DRDOs
- Carried out Design, analysis, Verification & Validation and Generation of code coverage report on FPGA Virtex-5 and Artex-7 of VHDL/Verilog based projects using Mentor Graphics Questa Prime, Xilinx tool.

Achievements

- Released the guidelines for FPGA based projects "Formulation of Guidelines for Development Life Cycle of FPGA based Design", Released on December 2015
- 2. IEEE Paper, "Independent Verification and Validation of FPGA based Design for Airborne Electronic Applications", December 2018

Guest Lecture

Embedded Systems & Design

Parithosh Vema

The EC Department had organized a guest lecture on 11th April 2019 in Embedded Systems & Design. The speaker for the lecture was Mr. Kulbhushan Bhaji Patariya who is a Scientist from Aeronautical Development Agency (ADA), Bangalore. He has over 8 years of experience in Software verification of Avionics Line Replacement unit for aircrafts and is very skilled in various hardware level languages. He also contributed towards the development of "DRDO standard for Software Development Guideline". This guideline is developed by DRDO for software development to be followed throughout all the DRDOs.

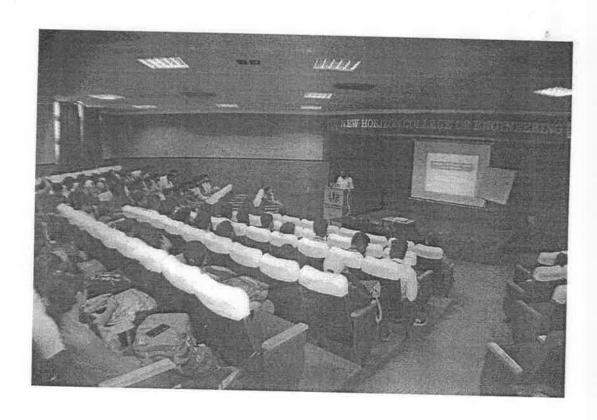


In his lecture the speaker started off by giving a brief introduction on what an embedded system is, how they are classified in different fields features. The session was very

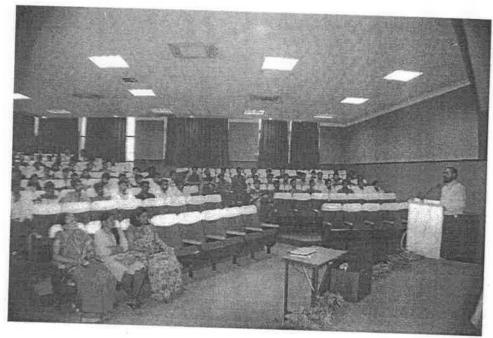
interactive, Mr. Kulbhushan told about his line of work, where the quality factors of an embedded system played an important role. As the factors were about real time situations, individual system check, behavior of the company and other factors.

He discussed the various factors by showing us a couple of videos which taught us that quality is more important than quantity, how teamwork and proper communication plays an important role.

The speaker discussed about the weapons integration on the line control unit along with the software and hardware integration in aircrafts.



He discussed about the working of aircraft pylons, he explained everything briefly such that all the students understood it. He concluded the session with a Q&A session with the students and the



teachers.

The sessionended with a vote of thanks and a felicitation to the speaker by the teacher coordinators. Mr. Kulbhushan was also presented with a sapling by the department.

Embedded System Conducted on
11/4/19

NEW HORIZON COLLEGE OF ENGINEERING DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING VI Sem "B" Sec

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3	1NH15EC007	A VIKRAM KUMAR REDDY	
4	1NH15EC075	R RAGHAV SRIVATSAV	Lagran
5	1NH15EC434	SUDHANYA S	- Mary
6	1NH15EC736	RISHAV PAUL	
7	1NH16EC702	ABHISHEK KHOT	(H)
8	1NH16EC703	ABHISHEK PATTAJOSHI	
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11	1NH16EC706	AMMIDAL VISHNU VIKAS	(NH)
12	1NH16EC707	ANITA CHAUHAN	
13	1NH16EC708	ANJU GOPINATH	
14	1NH16EC709	ASHUTOSH MANISH	
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4	1NH16EC001	A M AMARJITH	Par
2	1NH16EC002	ABHISHEK KUMAR DESHETTI	Present
3	1NH16EC003	ADITYA C	1
4	1NH16EC004	AFZAL HUSSAIN	
5	1NH16EC005	AKHIL JOSEF	
58	1NH16EC006	AKHILESH VARMA KALIDINDI	poulseret
4	1NH16EC007	AKSHAY RAO	Present
18	1NH16EC008	ANMOL KUMAR	
9	1NH16EC009	ANOOP.A	
40	1NH16EC010	ANUSHA H N	Present
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V18	1NH16EC019	C.LEHARIKA	Present
18	1NH16EC020	CHETHAN S.	Present
20	1NH16EC023	DEEPAK KU. PRADHAN	
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23	1NH16EC028	ELLINKI JAHNAVI	
134	1NH16ECQ30	GAVINASH	Present
25	1NH16EC031	GIRISH JATTU GOUDA	Present
126	1NH16EC032	HARI PRASAD.R	Peresont
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43	1NH16EC052	MAURYA REDDY	
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157	1NH16EC138	ASHISH KUMAR . S	Present
158	1NH16EC139	KARTHIK . K. S	Present
159	1NH17EC400	AKASH PRASAD C	Present
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NAME OF THE EVENT/CLUB: Application of Embedded system

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NAME OF THE EVENT/CLUB: Emledded System design

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