



NEW HORIZON COLLEGE OF ENGINEERING

AUTONOMOUS COLLEGE Permanently Affiliated to VTU, Approved by AICTE & UGC
Accredited by NAAC with 'A' Grade

Notice

Date: 10-04-2019

- 249
- All the IV semester students are hereby informed to attend the Expert Lecture on "Synthesis on FPGA" for the course System Design using HDL (ECE43) at Falconry Seminar Hall at 12.30 PM on 13/04/2019.

Alcelton
12/4/19
Faculty coordinator

[Signature]
HOD ECE

Shashikanth Patil

#634, 2nd Floor, 7th Mainroad, Vijayanagar, Bengaluru – 560040

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Career Objective:

To work in an innovative, challenging environment & help organization to achieve its objective in the most efficient manner by applying my skills, abilities and thereby growing with the organization.

Profile Summary:

- 8.5+ years of experience in FPGA/ASIC – Design, RTL Simulation, Synthesis, Creating testbench environment & Verification. Additional 2.2+ years of experience in Training & Academics.
- Experience in IP/Block Level Verification with Constraint Random, Assertions & Functional Coverage; creating verification environment with SystemVerilog, Verilog/VHDL & UVM/OVM.
- Experience with EDA Tools such as Synopsys – VCSMX, Verdi/ DVE; QuestaSim v10.1c, Xilinx ISE v12.1 and FPGA – Spartan 3E, Virtex 5; Programming & Scripting with MATLAB, C & Perl.
- Working knowledge on Peripheral Interface & OnChip Protocols – AMBA AXI/AHB, USB 3.x, SPI and UART.
- Working knowledge on complete Wind-Profiler Radar Sub-Systems and experience with Lab Equipments such as Signal Generator, Oscilloscope, Spectrum Analyzer and Network Analyzer.

Professional Experience:

- WIPRO Technologies Ltd., Bengaluru
Designation: *Team Lead* Duration: Jan 2015 to till date
- Spanwave Technology Solutions Pvt. Ltd., Bengaluru
Designation: *Senior Engineer* Duration: July 2010 to Dec 2014
- New Horizon College of Engineering., Bengaluru
Designation: *Teaching Staff (Lecturer)* Duration: Feb 2009 to May 2010
- C2C Semiconductors Pvt. Ltd., Bengaluru
Designation: *Member Technical Staff* Duration: April 2008 to Jan 2009

Education Qualification:

- M.Tech (*Embedded Electronic Systems*) from Visvesvaraya National Institute of Technology (NIT/REC), Nagpur (Maharastra, INDIA) during 2005 – 07.
- B.E. (*Electronics & Communication Engineering*) from Visvesvaraya Technological University, Belgaum (Karnataka, INDIA) during 2001 – 05.

Achievement : Qualified GATE – 2005 with 97.5 percentile (All India Rank – 915)

I hereby declare that the above particulars are true and correct to the best of my knowledge.

SHASHIKANTH PATIL

Report on Expert Lecture

The department of ECE conducted an expert lecture on Synthesis of FPGA for the course System Design Using HDL by Mr Shashikanth Patil , Team Lead , WIPRO Technologies Ltd., Bengaluru on 13/04/2019. The guest speaker spoke about the basics of FPGA and other target devices. He also spoke about different types of FPGA's like xilinx, microsemi etc. But mainly he discussed about xilinx because we are using it in our labs. Firstly he spoke about the basics of verilog and VHDL and how these codes can be dumped into the FPGA. He introduced the students about practical applications of topics related to our core subjects. He started off with the difference between verilog and VHDL and why verilog is more widely used. Then he discussed about the syntax of verilog as well as the keywords and the different modelling styles in verilog. The he explained a few basic programs. He spoke about simulation and synthesis and also how the code can be converted to hardware by the process of mapping placing and routing.

He also showed us the process to dump the code in the hardware. After typing the code in the xilinx suit, then the syntax check to check if there were any errors, later after entering the ucf , he explained the process of simulation and how to map, place and route and then how to connect the hardware kit to the computer and then he taught us how to implement the written code on the FPGA kit. He also explained that verilog is a very important code in the core electronics companies and FPGA is widely used. He told us that the basics we learn are very important. He also told us the various applications of FPGA used in industry and the advantages and disadvantages as well.

Hence I conclude that the lecture was very helpful and helped the students gain a lot more interest in this topic. It was very explanatory and relatable. It made the students understand various topics practically.

NEW HORIZON COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV Sem "A" Sec

Expert lecture attendance

Course: ECE43 System Design Using HDL

13/04/2019

SL.No	USN	NAME	Stream	Batch	Signature
1	1NH16EC038	KARROTU PRUDHVI	EXE	A1	
2	1NH17EC001	AARTHY P JAITH	EXE	A1	
3	1NH17EC002	ADESH V	PROF	A1	
4	1NH17EC003	ADITYA OMPRAKASH CHOUDHARY	EXE	A1	
5	1NH17EC004	AKSHITHA R	EXE	A1	<i>Akshitha</i>
6	1NH17EC005	ANAND T	EXE	A1	
7	1NH17EC006	ANAND T	PROF	A1	
8	1NH17EC007	ANKIT KUMAR	EXE	A1	
9	1NH17EC008	ARUN Y S	EXE	A1	
10	1NH17EC009	B S ABHISHEK	EXE	A1	
11	1NH17EC010	B YAMINI	EXE	A1	<i>JB</i>
12	1NH17EC011	BHARATH M	PROF	A1	
13	1NH17EC012	BHARATH Y R	EXE	A1	<i>Bharath Y</i>
14	1NH17EC013	BHASKAR K	EXE	A1	
15	1NH17EC014	BOGGARAPU MOURYA YASWANATH	EXE	A2	
16	1NH17EC015	BOREDDY NAGA MAHESWAR	EXE	A2	
17	1NH17EC016	C O PRUTHVI	EXE	A2	<i>Pruthvi</i>
18	1NH17EC017	CHANDANA N	PROF-G	A2	
19	1NH17EC018	CHETHAN B R	PROF-G	A2	
20	1NH17EC019	DEEKSHITH N REDDY	PROF-G	A2	
21	1NH17EC020	DEEPAK S	EXE	A2	
22	1NH17EC021	DEVASHRUTHA S	PROF	A2	
23	1NH17EC022	DHANYASHREE V REDDY	EXE	A2	<i>Danya Hara</i>
24	1NH17EC023	DIVYA SAGAR REDDY	EXE	A2	<i>Divya</i>
25	1NH17EC024	DUDDUKURI VAISHNAVI	EXE	A2	
26	1NH17EC025	G ASAD	EXE	A2	
27	1NH17EC026	G R VARSHA	EXE	A2	<i>Varsha</i>
28	1NH17EC027	GAGAN B K	EXE	A2	
29	1NH17EC028	GAUTAM SINHA	EXE	A2	
30	1NH17EC029	GIRIDHARAN P K	EXE	A2	
31	1NH17EC030	H K BHASKAR	EXE	A2	
32	1NH17EC031	HARSHITHA G	EXE	A2	<i>G Harsha</i>
33	1NH17EC032	IVATURI S S HARSHAVARDHAN	EXE	A2	<i>Harsha</i>
34	1NH17EC033	J RUTH SHARON	EXE	A2	
35	1NH17EC034	JERIN JOHNSON	EXE	A2	<i>Jerin</i>
36	1NH17EC035	JITENDER	EXE	A2	
37	1NH17EC036	K S SIRISH	PROF	A2	
38	1NH17EC037	K S SNEHA LATHA	EXE	A2	
39	1NH17EC038	KARAN SRINIVAS	EXE	A3	
40	1NH17EC039	KAVYASHREE S	EXE	A3	

41	1NH17EC040	KEVINE PRASANNA KUMAR	PROF-G	A3	
42	1NH17EC041	KUSHALA C	EXE	A3	
43	1NH17EC042	M BHARAT	EXE	A3	
44	1NH17EC043	M HEMANTH YADAV	EXE	A3	
45	1NH17EC044	MADALA HIMAJA	EXE	A3	
46	1NH17EC045	MADHUMITHA R	EXE	A3	<i>Madhuma</i>
47	1NH17EC046	MADHUPARNA BANERJEE	PROF	A3	
48	1NH17EC047	MAMATHA B S	EXE	A3	<i>Mamatha</i>
49	1NH17EC048	MANIKANTA	PROF	A3	
50	1NH17EC049	MANISH B	PROF	A3	
51	1NH17EC050	MANISHA R	EXE	A3	
52	1NH17EC051	MILAN SHANKAR RAO M	EXE	A3	
53	1NH17EC052	MOHAMMED GHASSAN	PROF	A2	
54	1NH17EC053	MONIKA K REDDY	EXE	A3	
55	1NH17EC054	NANDURI SRILASYA	EXE	A3	
56	1NH17EC055	NAVAJITH R REDDY	EXE	A3	
57	1NH17EC056	NAVEEN K M	EXE	A3	<i>Naveen</i>
58	1NH17EC057	NETTEM PARNITHA	EXE	A3	
59	1NH17EC058	NIKITH BABU	EXE	A3	
60	1NH17EC059	NISHANTH C	PROF	A3	
61	1NH17EC127	SUMAN PAL	EXE	A1	
62	1NH17EC128	SUHAIL PASHA	EXE	A1	
63	1NH17EC129	BATHULA SRI SAI KRISHNA	EXE	A1	
64	1NH17EC130	ANISHA DEVI	EXE	A1	
65	1NH17EC131	ASHWIN S P	EXE	A3	
66	1NH17EC132	MURAKONDA AVINASH	EXE	A1	
67	1NH17EC133	THURPUNATI SURYA PRAKASH	EXE	A1	
68	1NH17EC135	SAI KARTHIK P R	PROF	A1	
69	1NH17EC137	GANGADHARA VARSHITHA	PROF	A1	
70	1NH18EC405	Charan S	Prof	A3	<i>Charan S</i>
71	1NH18EC407	Nagesh N Reddy	Prof	A1	<i>Nagesh N Reddy</i>
72	1NH18EC408	Pragati Shrinivas Naik	Prof	A1	<i>Pragati Shrinivas Naik</i>
73	1NH18EC409	Prashant Joshi	Prof	A3	<i>Prashant Joshi</i>
74	1NH18EC410	Ravi S	Prof	A3	<i>Ravi S</i>
75	1NH18EC414	Santosh B Gonal	Prof	A2	<i>Santosh</i>
76	1NH18EC417	Spoorthi S T	Prof	A1	
77	1NH18EC418	Swaroop M R	Prof	A1	
78	1NH18EC421	Vinod K	Prof	A1	
79	1NH18EC422	Yashwanth N K	Prof	A2	<i>Yashwanth</i>
80	1NH18EC424	Naveen N	Prof	A3	<i>Naveen</i>
81	1NH18EC425	Nikhil Gopal Krishna Sirsikar	Prof	A3	<i>Nikhil</i>
82	1NH18EC428	Sandeep Reddy V	Prof	A2	<i>Sandeep</i>
83	1NH18EC432	Vishal Nayak	Prof	A1	<i>Vishal</i>

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IV Sem "C" Sec

Expert lecture attendance

Course: ECE43 System Design Using HDL

13/04/201

SL.No	USN	NAME	Stream	Batch	Dated
1	1NH16EC021	D Gowtham	EXE	C3	
2	1NH16EC065	NILOTHPAL	EXE	C3	
3	1NH16EC140	Karthik K	PROF	C3	
4	1NH16EC701	A NAVYA SHREE	EXE	C3	<i>Kavya</i>
5	1NH17EC700	AATHIRA VIJAYAN	EXE	C1	<i>Aathira</i>
6	1NH17EC701	AJAY S	EXE	C1	
7	1NH17EC702	AKSHAJA SHIMALNA O K	EXE	C1	<i>Akshaja</i>
8	1NH17EC703	ARAVIND P M	PROF	C1	
9	1NH17EC704	ASHIKA P	EXE	C1	<i>Ashika</i>
10	1NH17EC705	BHARGAVI	EXE	C1	
11	1NH17EC706	CHANDU V J	PROF-G	C1	
12	1NH17EC707	DHANUSH S	EXE	C1	
13	1NH17EC708	DHINAKARAN S	EXE	C1	
14	1NH17EC709	DISHANTH M P	PROF	C1	
15	1NH17EC710	G DEEPIKA	EXE	C1	<i>Deepika</i>
16	1NH17EC711	GAGANASHREE P S	EXE	C1	<i>Gagan</i>
17	1NH17EC712	GRACE GLADYS A	EXE	C1	<i>Grace</i>
18	1NH17EC713	HARSHITH NAIDU J	EXE	C1	
19	1NH17EC714	J BHAVANA	EXE	C1	<i>Bhavana</i>
20	1NH17EC715	JAYADEEP G P	EXE	C1	<i>Jayadeep</i>
21	1NH17EC716	JILLELLA RUPASAGAR REDDY	EXE	C1	
22	1NH17EC717	K AISWARYA	EXE	C1	<i>Aishwarya</i>
23	1NH17EC718	KONDURU SIVARAMA RAJU	EXE	C1	
24	1NH17EC719	KOTEPALLI RAMCHARAN	PROF	C1	
25	1NH17EC720	LAVANYA S N	EXE	C2	<i>Lavanya</i>
26	1NH17EC721	MANISHA M N	EXE	C2	<i>Manisha</i>
27	1NH17EC722	MANOHAR G	EXE	C2	
28	1NH17EC723	MANOJ M R	PROF	C2	
29	1NH17EC724	MUNAGALA BHARATH KUMAR	EXE	C2	
30	1NH17EC725	NAINI NARAMA	EXE	C2	
31	1NH17EC726	NAVEEN KUMAR M S	EXE	C2	
32	1NH17EC727	NEHA KRISHNA	EXE	C2	
33	1NH17EC728	NIDUJUVVI CHAKRADHAR	EXE	C2	
34	1NH17EC729	NIKHIL DWIVEDI	EXE	C2	

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35	1NH17EC730	P K KANAKA SHARANYA	EXE	C2	<i>[Signature]</i>
36	1NH17EC731	PALVADHI VENKATA SAI PAVAN	PROF-G	C2	
37	1NH17EC732	PAVAN U	EXE	C2	
38	1NH17EC733	PRACHI VERMA	PROF	C2	
39	1NH17EC734	PRAJWAL P	EXE	C2	
40	1NH17EC736	RAHUL BALAJI P S	EXE	C2	<i>[Signature]</i>
41	1NH17EC737	RISHIKA POTEPELLI	EXE	C2	
42	1NH17EC738	RITWIK SHOME	PROF	C2	
43	1NH17EC739	S DENILA GRACELIN	EXE	C2	<i>[Signature]</i>
44	1NH17EC740	S RAHUL YADAV	EXE	C2	
45	1NH17EC741	S SAI HARSHA	EXE	C3	
46	1NH17EC742	SAHIB ARORA	EXE	C3	
47	1NH17EC743	SANDHYA K	EXE	C3	<i>[Signature]</i>
48	1NH17EC744	SHARATH T P	PROF	C3	<i>[Signature]</i>
49	1NH17EC745	SHARON MOSES	PROF-G	C3	
50	1NH17EC746	SHASHANK REDDY J	EXE	C3	
51	1NH17EC747	SHASHANK S SALOTAGI	PROF	C3	
52	1NH17EC748	SHIVANG DUBEY	EXE	C3	
53	1NH17EC749	SIRISHA B S	EXE	C3	<i>[Signature]</i>
54	1NH17EC750	SOMA YASWANTH REDDY	EXE	C3	<i>[Signature]</i>
55	1NH17EC751	SREEKAR REDDY KALLAM	EXE	C3	
56	1NH17EC752	SUHAS S	EXE	C3	
57	1NH17EC753	SYED TOUQEER	EXE	C3	
58	1NH17EC754	THUMMALA SURYA DEVI	EXE	C3	
59	1NH17EC755	USTHEPALLI ABHIRAM	EXE	C3	<i>[Signature]</i>
60	1NH17EC756	VARUN P	PROF	C3	<i>[Signature]</i>
61	1NH17EC757	VELURI SRI HARSHAVARDHAN	PROF	C3	<i>[Signature]</i>
62	1NH17EC758	Y SUCHARITHA	EXE	C3	<i>[Signature]</i>
63	1NH17EC759	YERRABALLI GURU SAI KUMAR	EXE	C3	<i>[Signature]</i>
64	1NH18EC402	Aiyappa K K	Prof	C3	<i>[Signature]</i>
65	1NH18EC406	Harshitha M	Prof	C3	<i>[Signature]</i>
66	1NH18EC411	Roja Parameshwar Ullikashi	Prof	C3	<i>[Signature]</i>
67	1NH18EC415	Sathyaprasada G P	Prof	C3	<i>[Signature]</i>
68	1NH18EC419	Tejaswini T R	Prof	C3	<i>[Signature]</i>
69	1NH18EC091	Sam Leander	EXE	C2	<i>[Signature]</i>

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