

Autonomous College Permanently Affillated to VTU, Approved by AICTE & UGC Accredited by NAAC with 'A' Grade, Accredited by NBA

OFFER LETTER

Ref. No.: NHCE/OFFER/85/2019

September 20th, 2019

Mr. Harish Kumar Villuri, Component Design Engineer Intel Technologies Bangalore.

Dear, Mr. Harish Kumar Villuri,

Congratulations! We are pleased to appoint you as an Adjunct Faculty in the Department of Electronics and Communication Engineering in New Horizon College of Engineering for the academic year 2019 -20 New Horizon College of Engineering is a unit of New Horizon Educational Institution, founded in 1970 to provide quality education.

• The subject allotted to you is CMOS VLSI Design (Module 4: Timing Analysis).

Adjunct faculty appointments are temporary in nature and are usually made for one year. You may be considered for reappointment in accordance with existing policy. The Institution reserves the right to reschedule or reassign the teaching responsibilities for this appointment.

Kindly acknowledge the receipt of this offer letter and also the acceptance of the offer.

We welcome you to New Horizon family.

Head - Human Resources

Accepted the offer as per the terms discussed.

Signature: V. Hasish Kumas,

Date: 20/09/19

S.No	DETAILS REQUIRED				
I	Name of the Resource person	Harish Kumar Villuri			
2	Years of Experience	9 years			
3	Currently working in	Intel Technologies, Bangalore			
4	Designation	Component Design Engineer			
5	Number of Lecture Hours	3			
6	Date of Lecture	20/09/2019			
7	PAN Number	APIPV9932R			
8	Aadhar Number	502084543468			
9	Date of Birth	22-01-1989			
10	Educational Qualification	B Tech			
11	Bank Name	State Bank of India			
12	Account Number	00000020106981121			
13	IFSE Code & Branch	SBIN0017042,THUBARAHALLI BRANCH			

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NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

18/09/2019

GUEST LECTURE

This is to inform to V semester students of ECE that the guest lecture is arranged on 20-09-2019 at Chanakya seminar hall between 10:00AM to 1:00PM.

Topic to be delivered: CMOS VLSI Design

Name of the Speaker: Mr. Harish Kumar Villuri

Designation

: Component Design Engineer

Company

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: Intel technologies

Respective subject teachers are hereby informed to be present at the Chanakya seminar hall during the lecture.

All the faculty members are invited to attend the same without affecting the regular classes.

Guest Lecture coordinator

HoD-ECE

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NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Report on Guest Lecture

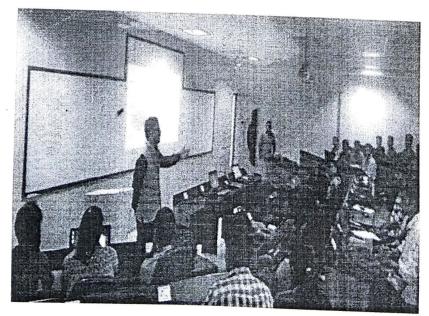
Topics: CMOS VLSI Design

A Guest lecture on CMOS VLSI Design was conducted on 20th September 2019 by Mr. Harish Kumar villuri who is working at Intel Technologies, Bangalore.

He briefed that Over the past several years, Silicon CMOS technology has become the dominant fabrication process for relatively high performance and cost effective VLSI circuits. The revolutionary nature of these developments is understood by the rapid growth in which the number of transistors integrated on circuit on single chip.

The electronics industry has achieved a phenomenal growth over the last few decades, mainly due to the rapid advances in large scale integration technologies and system design applications. With the advent of very large scale integration (VLSI) designs, the number of applications of integrated circuits (ICs) in high-performance computing, controls, telecommunications, image and video processing, and consumer electronics has been rising at a very fast pace.

Overall the session was interactive. The session was organized by Dr. Sanjeev Sharma, HoD, Dept of ECE along with the faculty coordinators Ms. Divya Sharma and Ms. Dharmambal.



NEW HORIZON COLLEGE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

V semester (A, B and C)

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